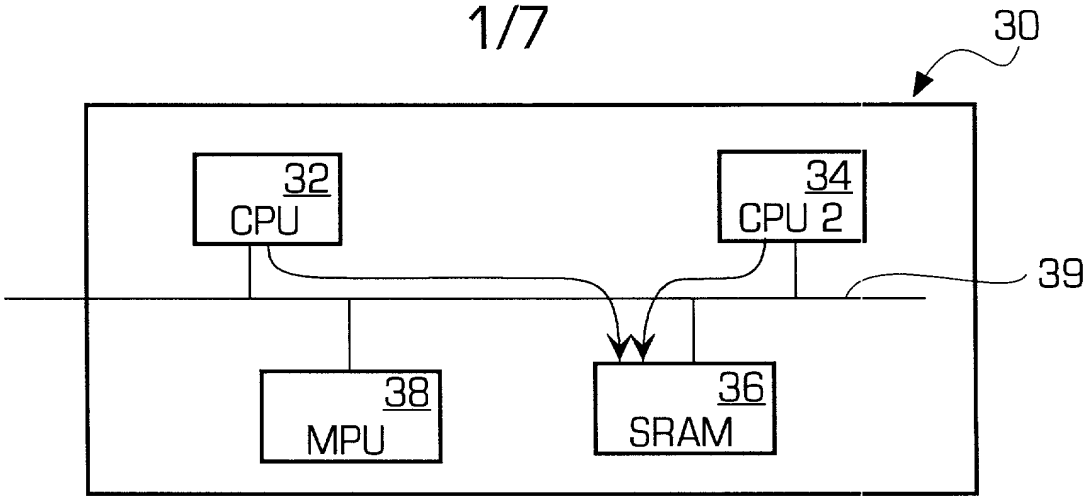

FIG. 1

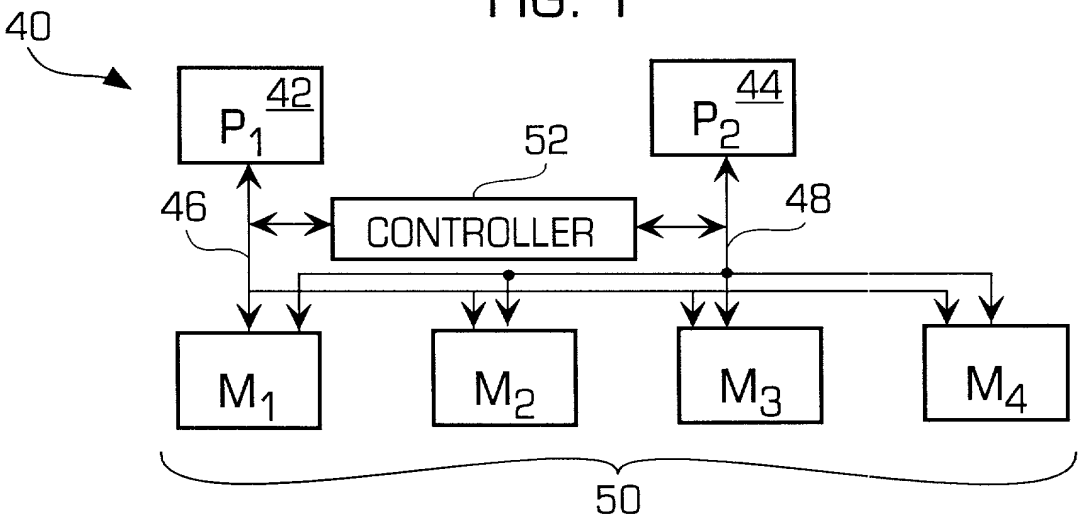

FIG. 2

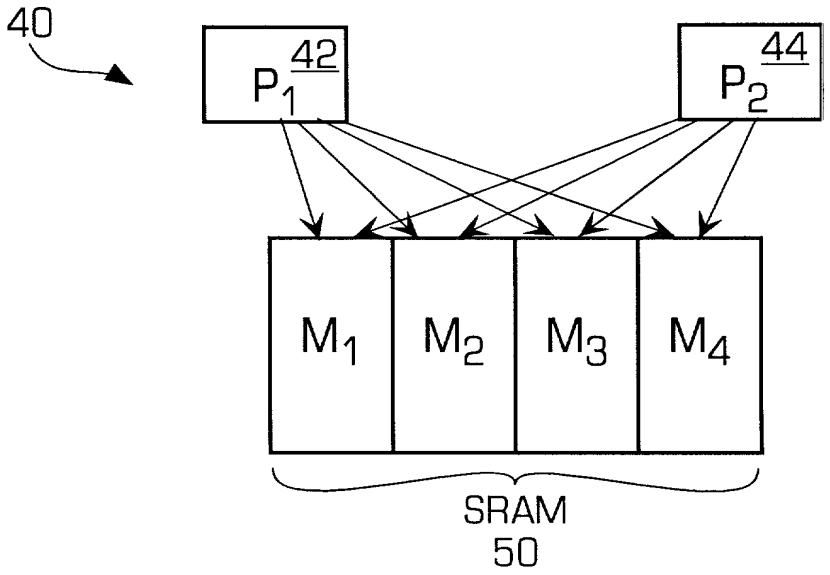

FIG. 3

09647951.080201

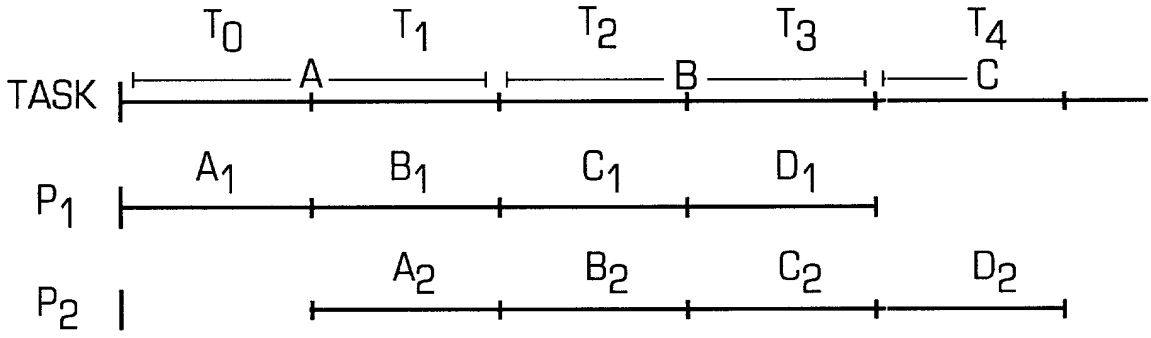

FIG. 4

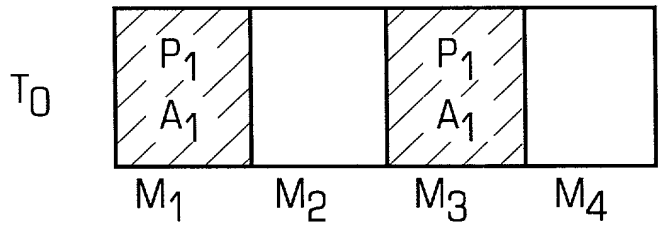

FIG. 5A

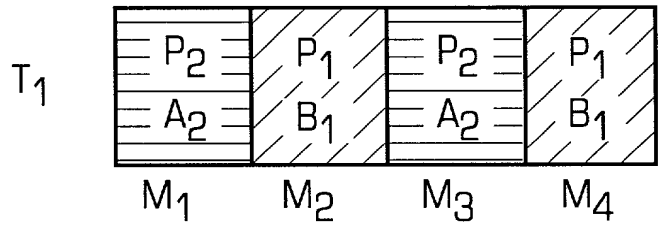

FIG. 5B

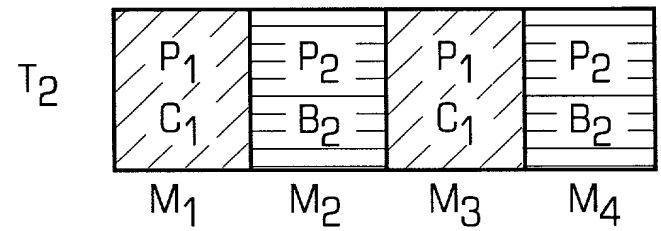

FIG. 5C

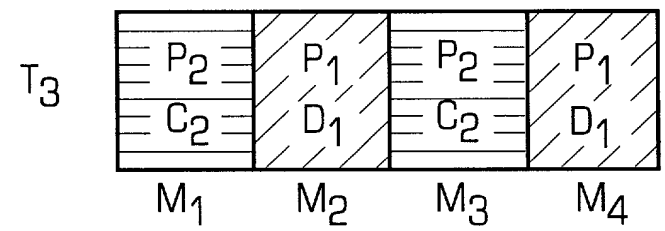

FIG. 5D

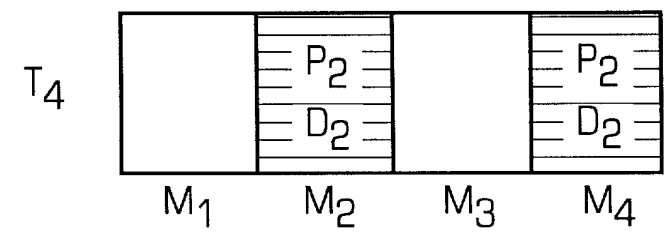

FIG. 5E

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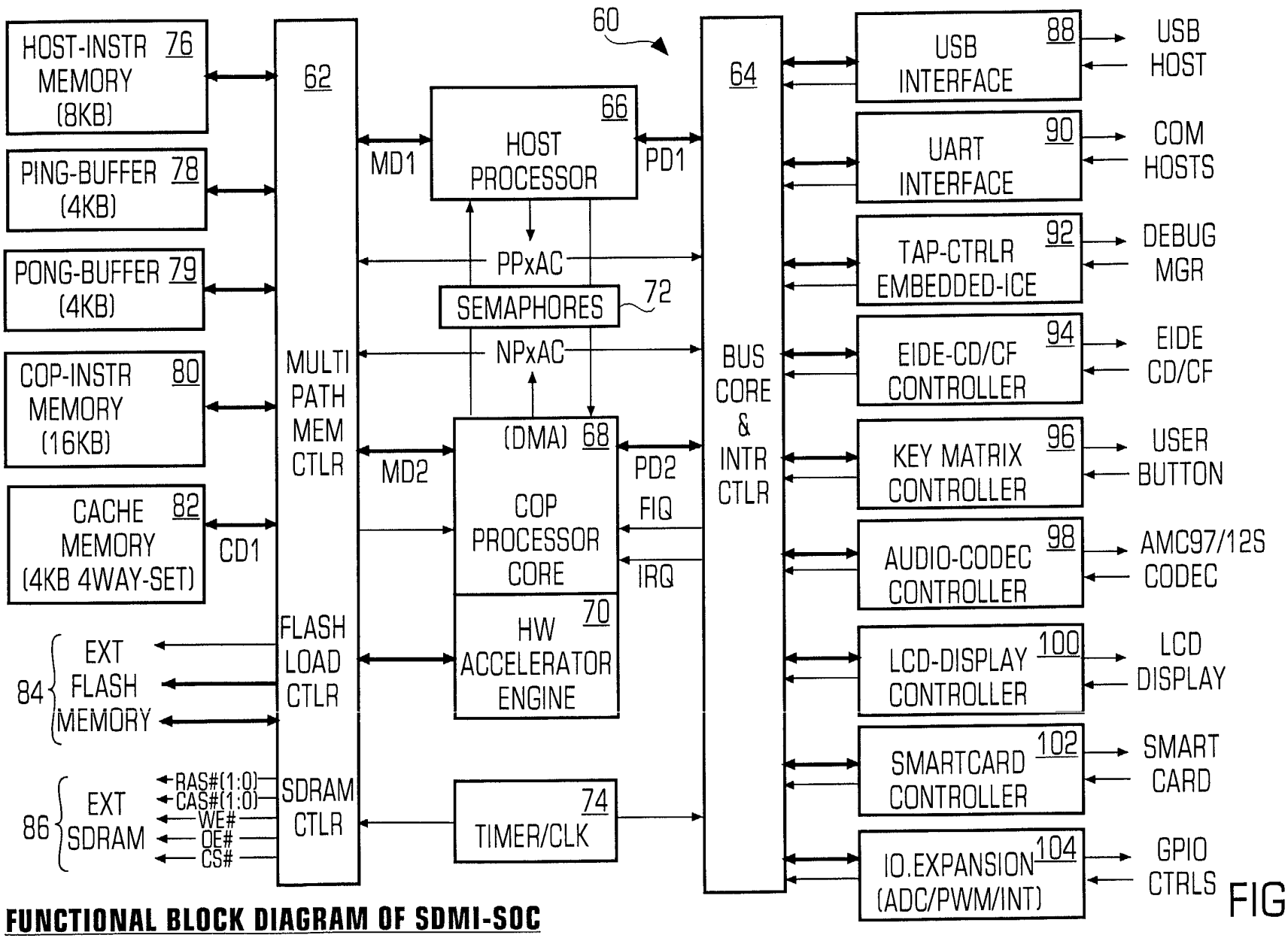
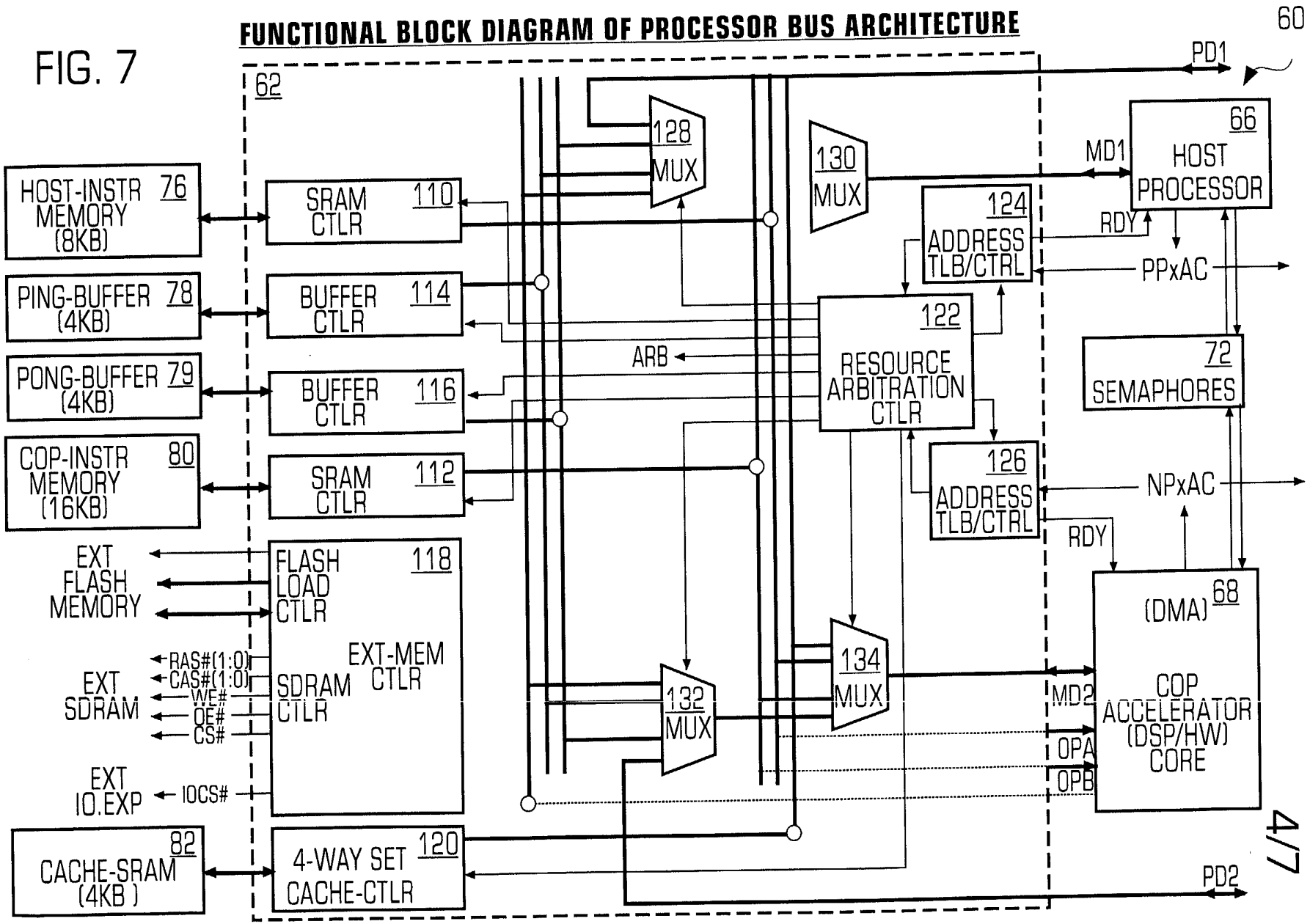


FUNCTIONAL BLOCK DIAGRAM OF SDMI-SOC

FIG. 6

T08080 T624860

FIG. 7



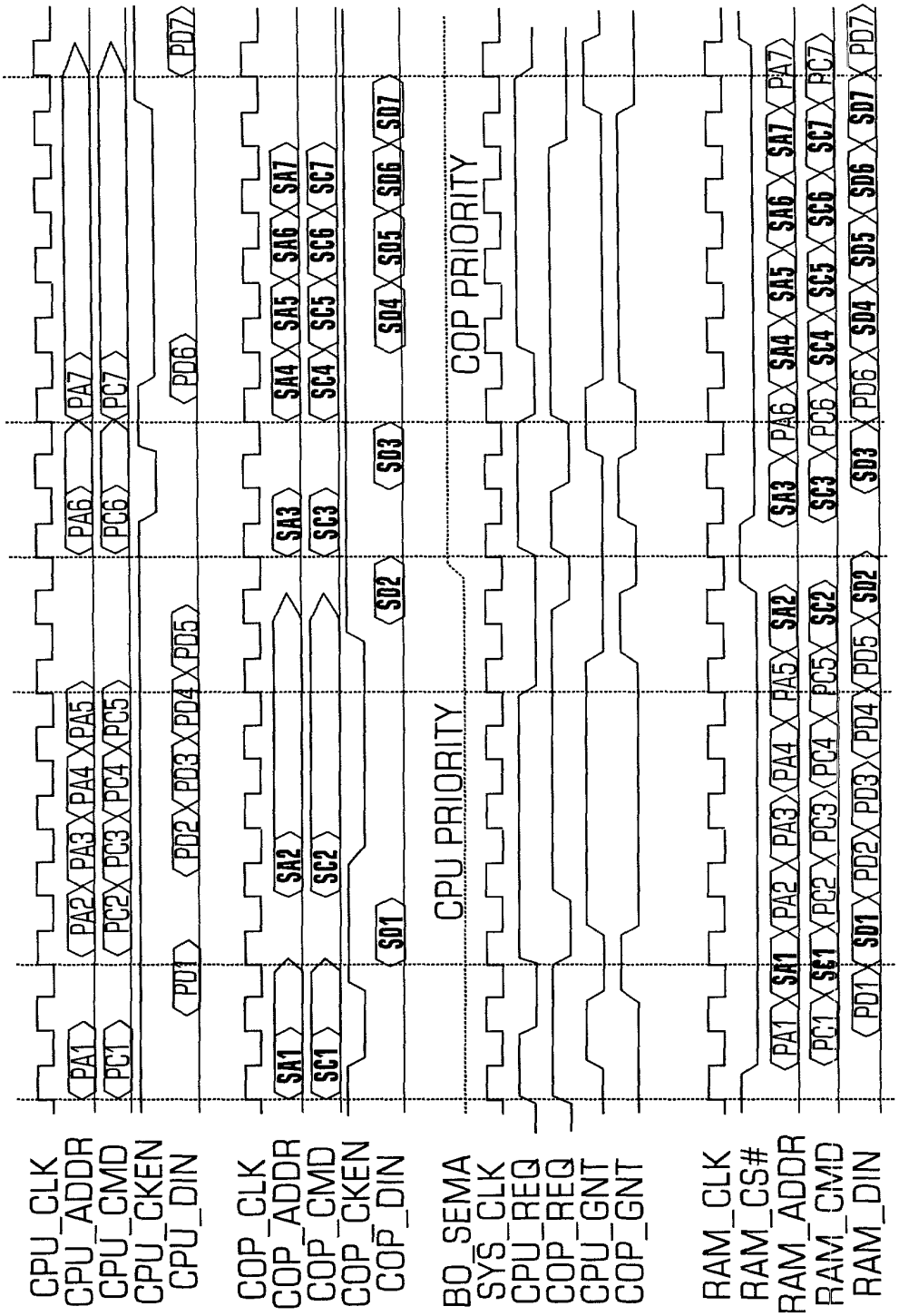

CPU_CLK
CPU_ADDR
CPU_CMD
CPU_CKEN
CPU_DIN
COP_CLK
COP_ADDR
COP_CMD
COP_CKEN
COP_DIN
BO_SEMA
SYS_CLK
CPU_REQ
COP_REQ
CPU_GNT
COP_GNT
RAM_CLK
RAM_CS#
RAM_ADDR
RAM_CMD
RAM_DIN
CPU PRIORITY
COP PRIORITY

FIG. 8

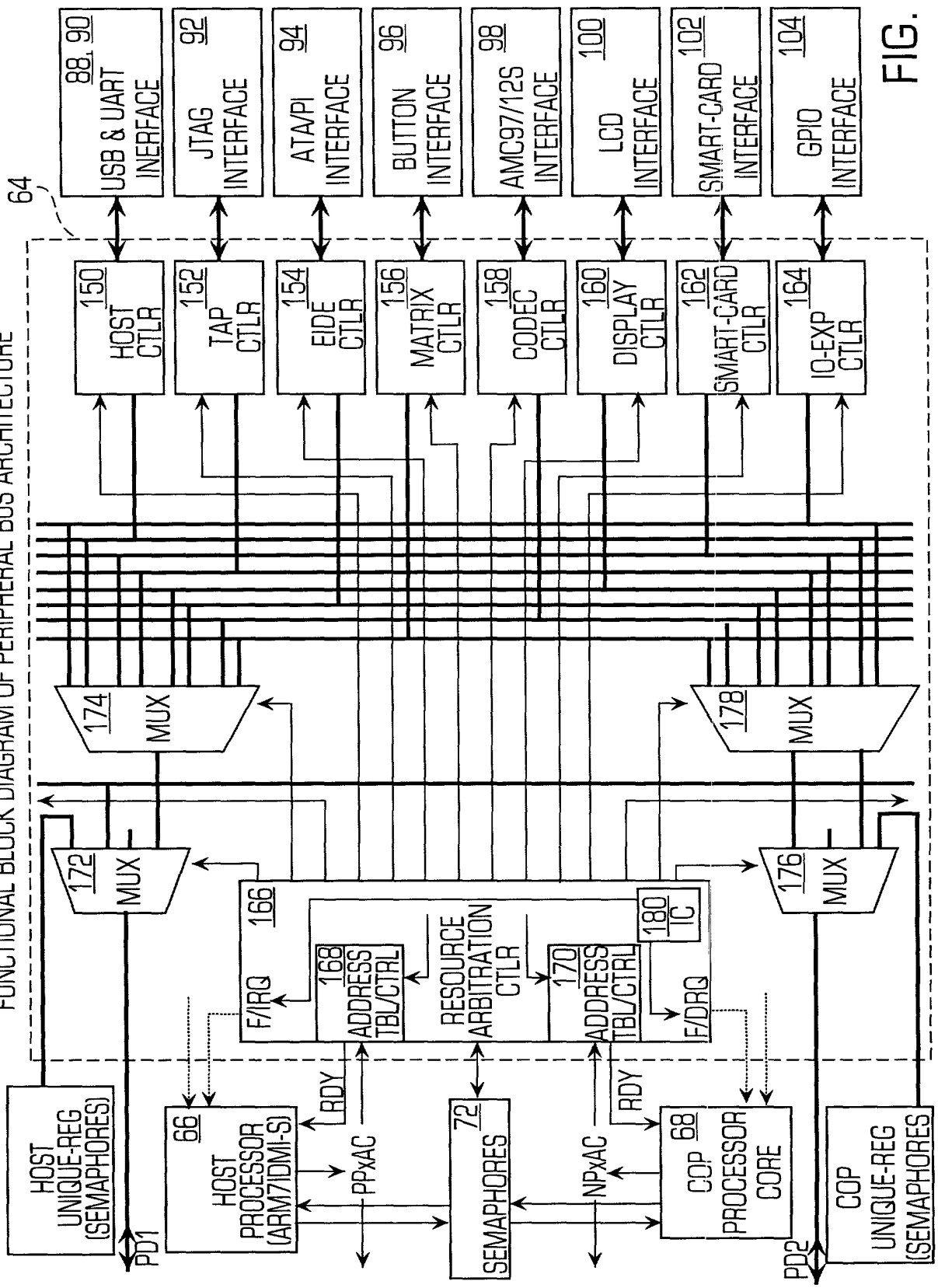

FUNCTIONAL BLOCK DIAGRAM OF PERIPHERAL BUS ARCHITECTURE
64
88, 90 USB & UART INERFACE
92 JTAG INTERFACE
94 ATA/PI INTERFACE
96 BUTTON INTERFACE
98 AMC97/12S INTERFACE
100 LCD INTERFACE
102 SMART-CARD INTERFACE
104 GPIO INTERFACE
150 HOST CTLR
152 TAP CTLR
154 EIDE CTLR
156 MATRIX CTLR
158 CODEC CTLR
160 DISPLAY CTLR
162 SMART-CARD CTLR
164 IO-EXP CTLR
174 MUX
178 MUX
172 MUX
176 MUX
166 F/IRQ
168 ADDRESS TBL/CTRL
RESOURCE ARBITRATION CTLR
170 ADDRESS TBL/CTRL
180 IC
F/DRQ
HOST UNIQUE-REG (SEMAPHORES)
66 HOST PROCESSOR (ARM7IDMI-S)
RDY
PPxAC
72 SEMAPHORES
NPxAC
RDY
68 COP PROCESSOR CORE
COP UNIQUE-REG (SEMAPHORES)
PD1
PD2

FIG. 9

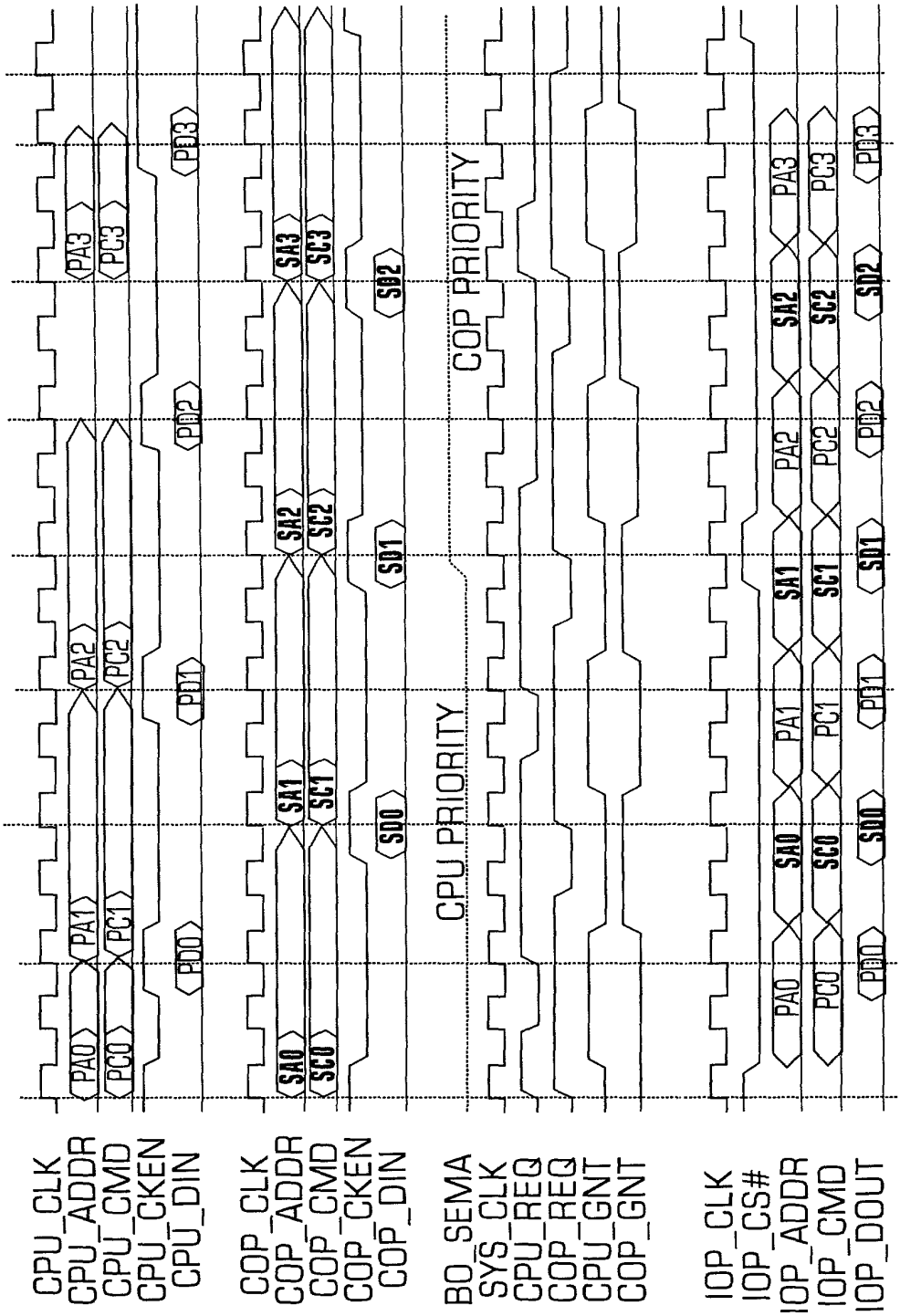

CPU_CLK
CPU_ADDR
CPU_CMD
CPU_CKEN
CPU_DIN
COP_CLK
COP_ADDR
COP_CMD
COP_CKEN
COP_DIN
BO_SEMA
SYS_CLK
CPU_REQ
COP_REQ
CPU_GNT
COP_GNT
IOP_CLK
IOP_CS#
IOP_ADDR
IOP_CMD
IOP_DOUT
CPU PRIORITY
COP PRIORITY
PA0
PA1
PA2
PA3
PC0
PC1
PC2
PC3
PD0
PD1
PD2
PD3
SA0
SA1
SA2
SA3
SC0
SC1
SC2
SC3
SD0
SD1
SD2
SD3

FIG. 10